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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

10/787,331

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Application / Conf. No.

TRANSMITTAL FORM (to be used for all correspondence after initial filing)		Filing Date	February 26, 2004
		First Named Inventor	Austin H. Lesca
		Examiner Name	Ly D. Pham
Mail Stop: AMENDMENT		Art Unit	2818
Express Mail Receipt No. VIA FACSIMILE		Patent No.	
Total Number of Pages In This Submission	T	Attorney Docket Number	X-1026-2D US
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Attn: Lois D. Cartier		(Customer Number)	Reg. Number 40,941
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Date May 21, 2004 Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040			
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I hereby certify that this correspondence is being sent Via Facsimile to the U. S. Patent Office, attention to Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on this date: May 21, 2004			
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X-1026-2D US 10/787,331

Conf. No. UHRACEIVED CENTRAL FAX CENTER

IN THE UNITED STATES PATENT OFFICE

MAY 2 1 2004

Applicant:

Austin H. Lesea

Assignee:

Xilinx, Inc.

Title:

Memory Cells Enhanced for Resistance to Single

Event Upset

Serial No.:

10/787,331

File Date:

2/26/2004

Examiner:

Ly D. Pham

Art Unit:

2818

Docket No.:

X-1026-2D US

Conf. No.:

Unknown

ATTN: Examiner Ly D. Pham

VIA FACSIMILE Fax: 571-273-1793

COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Before taking action in the above-referenced application, please enter the following amendments.